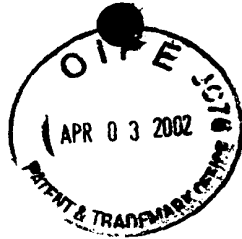


193414US2S



# 1/10 mdt a  
G. Patel  
4/26/02

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
Tatsuya KUNIKIYO : GAU: 2814  
SERIAL NO: 09/612,298 :  
FILED: JULY 7, 2000 : EXAMINER: CAO, PHAT X.  
FOR: SEMICONDUCTOR DEVICE

RECEIVED  
APR - 14 2002  
TC 2800 MAIL ROOM

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Responsive to the Office Action dated January 3, 2002, please amend the above-identified application as follows:

IN THE TITLE

Page 1, line 2, delete in its entirety and substitute therefor:

SEMICONDUCTOR DEVICE HAVING MULTILAYER  
INTERCONNECTION STRUCTURE

IN THE CLAIMS

Please cancel Claims 2-6 and 16-20 without prejudice.

Please amend the claims as indicated in the marked-up copy to read as follows:

10. (Amended) A semiconductor device comprising:

A 1  
a semiconductor substrate having a main surface along which a semiconductor  
element is formed;